

Davis Wright Tremaine LLP



ANCHORAGE BELLEVUE LOS ANGELES NEW YORK PORTLAND SAN FRANCISCO SEATTLE SHANGHAI WASHINGTON, D.C.

SUITE 800
505 MONTGOMERY STREET
SAN FRANCISCO, CA 94111-6533

TEL (415) 276-6500
FAX (415) 276-6599
www.dwt.com

March 3, 2011

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No. 66785

Re: Applicants: Chen et al.
Title: Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memories
Application No.: 10/552,948 Filing Date: September 11, 2006
Examiner: Connie C. Yoha Group Art Unit: 2827
Docket No.: SNDK.284US1 Conf. No.: 6753

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter; and
- (2) Power of Attorney by Assignee of Entire Interest (2 pages).

☒ No additional fee is required.

FILED VIA EFS

Respectfully submitted,

Michael G. Cleveland
Reg. No. 46,030